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Setting FMC Vadj of ZCU111 for CON-FMC

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Abstract

This document addresses how to set FMC Vadj of Xilinx ZCU111 board.

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1 Introduction

CON-FMC only supports Vadj voltage from 1.8V to 3.3V. CON-FMC has an IPMI EEPROM on board, which contains a set of computer interface specifications including FMC Vadj. It is expected carrier board to use the voltage that is specified in the IPMI EEPROM, but some carrier board does not compile with this.

Xilinx ZCU111 board must be configured to drive FMC Vadj 1.8V as follows.

2 Windows: System Controller

This step modifies configuration at boot stage by on-board system controller.

2.1 Get “ZCU111 System Controller GUI Tutorial” package

Visit Xilinx web-site and search one of followings.

- ZCU111 System Controller GUI Tutorial
- rdf0475

And then download 'rdf0475-zcu111-system-controller-c-2018-2.zip'.

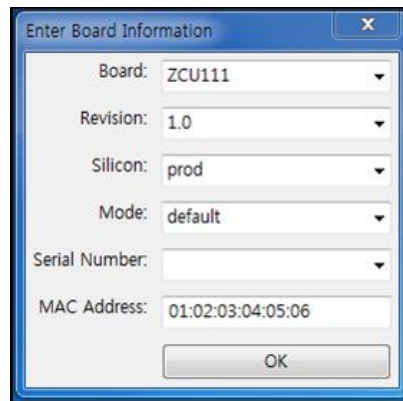
| XILINX | | | Applications | Products | Developers | Support |
|--|---|------|--------------|----------|------------|---------|
| Designs | | | | | | |
| Example Designs | Design Files | Date | | | | |
| ZCU111 Software Install and Board Setup Tutorial | | | | | | |
| ZCU111 Board Interface Test | rdf0469-zcu111-bit-c-2018-2.zip | | | | | |
| ZCU111 IBERT Tutorial | rdf0470-zcu111-ibert-c-2018-2.zip | | | | | |
| ZCU111 IPI Tutorial | rdf0471-zcu111-ipi-c-2018-2.zip | | | | | |
| ZCU111 MIG Design Files | rdf0472-zcu111-mig-c-2018-2.zip | | | | | |
| ZCU111 Restoring Flash Tutorial | rdf0473-zcu111-restoring-flash-c-2018-2.zip | | | | | |
| ZCU111 System Controller GUI Tutorial | rdf0475-zcu111-system-controller-c-2018-2.zip | | | | | |

2.2 Connect USB-to-JTAG to the computer

Connect ZCU111 board to the computer through USB port J83.

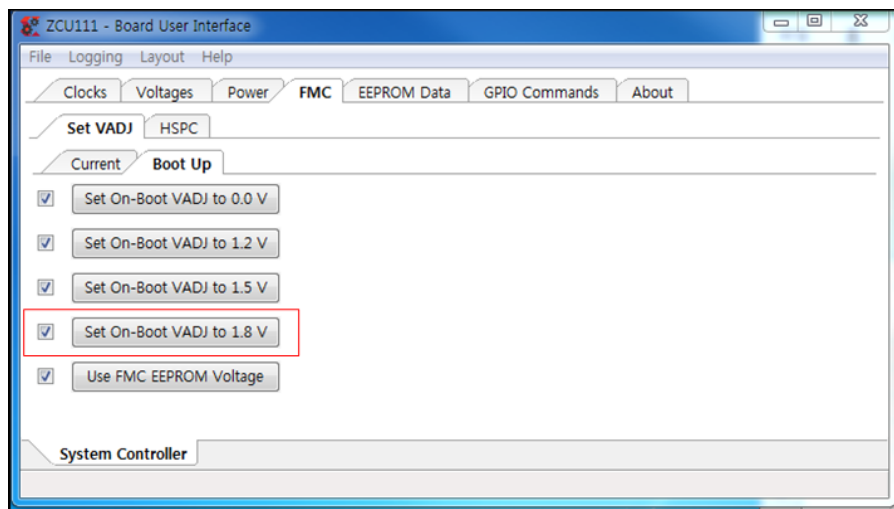
2.3 Decompress the ZIP file

Decompress the ZIP file and then invoke BoardUI.exe. Then select 'ZCU111'.



2.4 Select 1.8V for VADJ

Select 'Set On-Boot VADJ to 1.8V' from 'FMC->Set VADJ->BootUp' menu tab.



3 FSBL

When ZCU111 board brings up using uSD Card, FSBL should set board as expected, but current FSBL for ZCU111 may not be properly prepared yet, so makes FSBL skip the step to configure FMC board voltage.

This code makes ARM in the PS of carrier board not to set FMC_Vadj at the step of FSBL. This means the system controller should take care of FMC_Vadj as described previous section. Following code shows how to modify FSBL code; simply comment out the code.

```

702      /*For MISRA C compliance*/
703  }
704 #endif
705
706
707 // Status = XFSbl_FMCEnable(&I2c0Instance);
708 // if (Status != XST_SUCCESS) {
709 //     XFSbl_Printf(DEBUG_INFO, "FMC UADJ Configuration Not Successful");
710 // }
711 XFSbl_Printf(DEBUG_INFO, "Board Configuration successful\n\r");
712 UStatus = XFSBL_SUCCESS;
713
714 END:
715
716 return UStatus;
717 }
718
719
720 #if defined(XPS_BOARD_ZCU102)
721 /*****
722 /**
723  * This function is used to provide PCIe reset on ZCU102 board.
724  */

```

4 References

- [1] Xilinx, ZCU111 Evaluation Board, User Guide, UG1271, 2018.
- [2] Xilinx, ZCU111 RFSoc RF Data Converter Evaluation Tool Getting Started Guide, <https://xilinx-wiki.atlassian.net/wiki/spaces/A/pages/57606309/ZCU111+RFSoc+RF+Data+Converter+Evaluation+Tool+Getting+Started+Guide>
- [3] Xilinx, Package Location ZCU111, <https://xilinx-wiki.atlassian.net/wiki/spaces/A/pages/136085583/Package+Location+ZCU111>

Revision history

□ 2019.06.15: Started

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